



LUND
UNIVERSITY

EDAP15: Program Analysis

POINTER ANALYSIS 1



Christoph Reichenbach



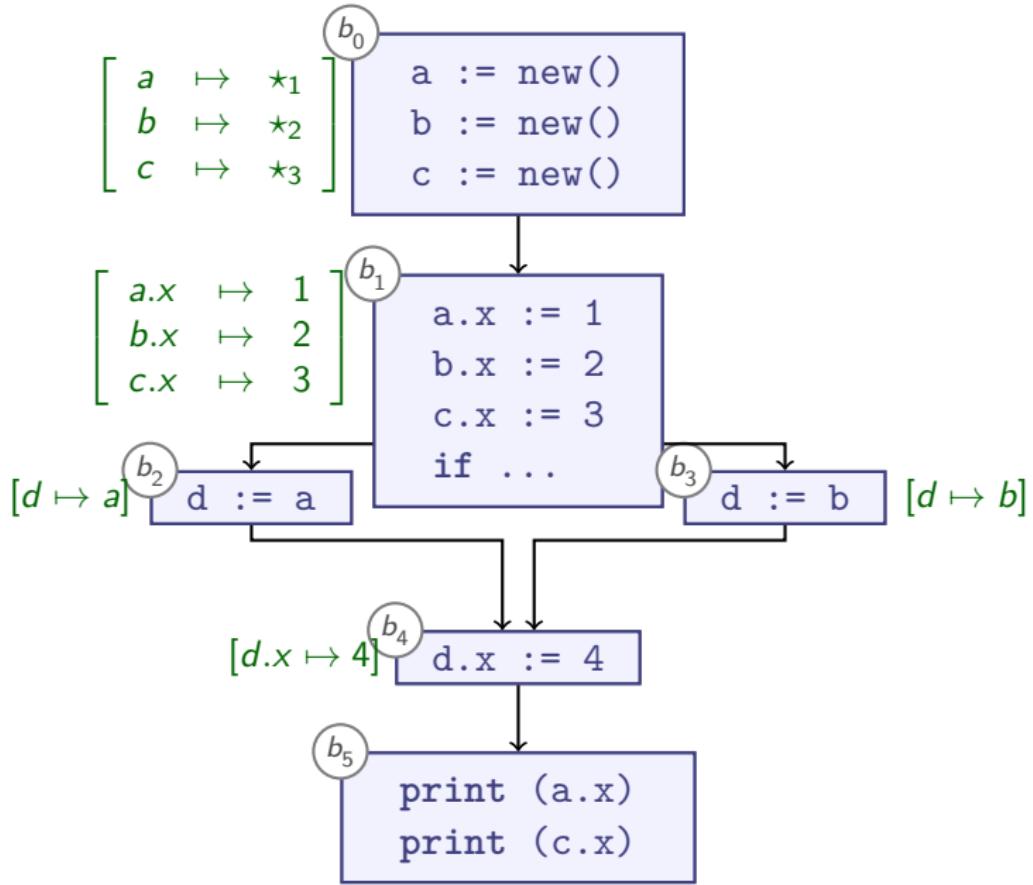
Welcome back!

- ▶ Questions?

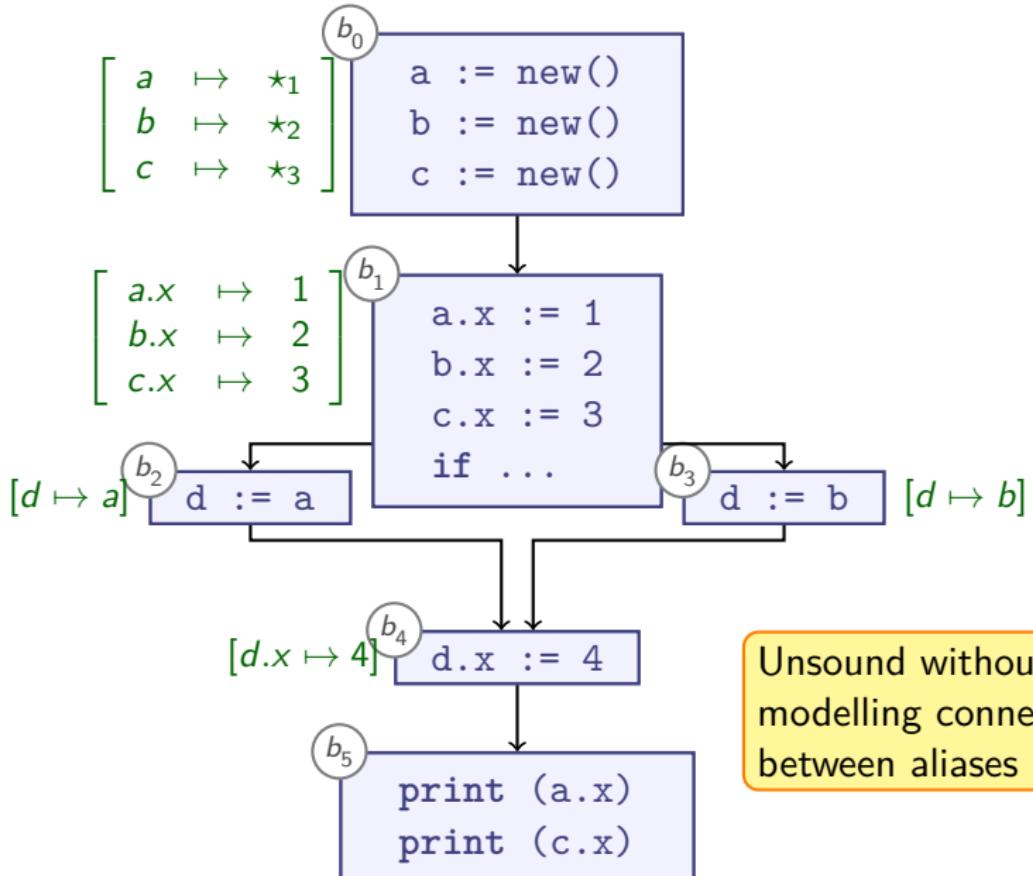
Teal-2

```
var x := new MyType();
x.field = value;
print(x.field);
```

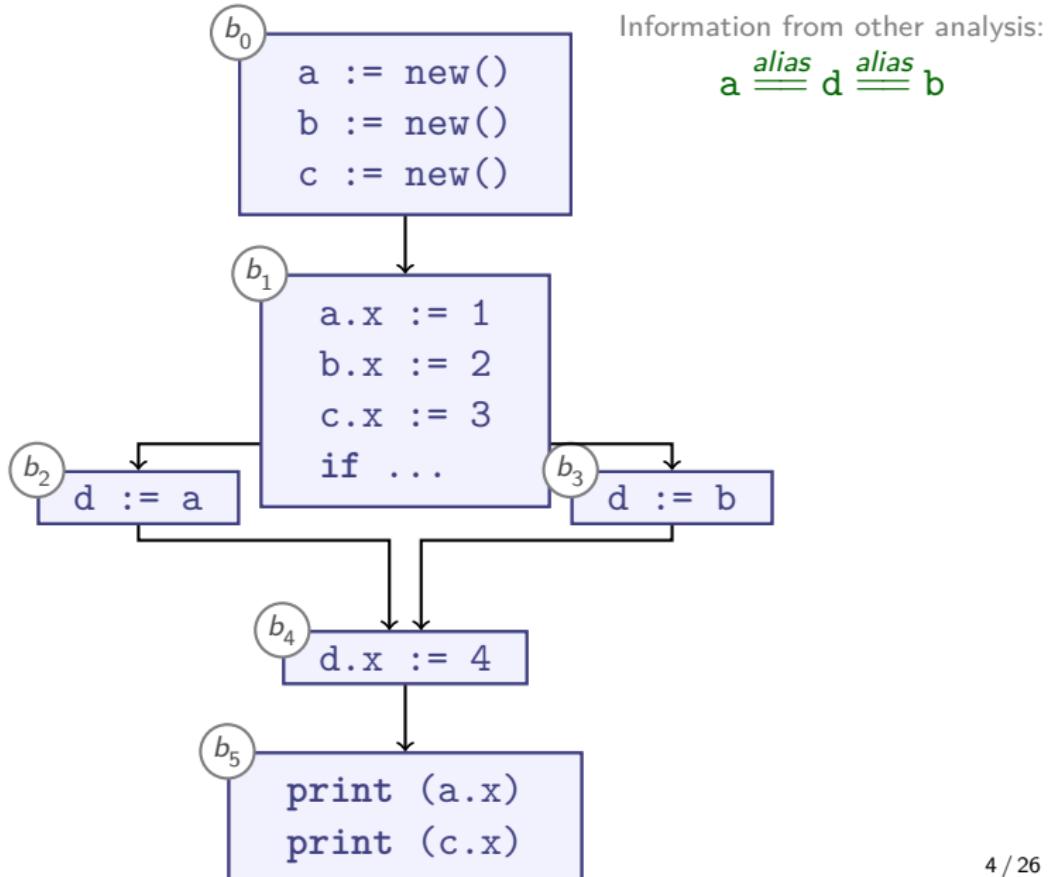
Dataflow with Alias Information



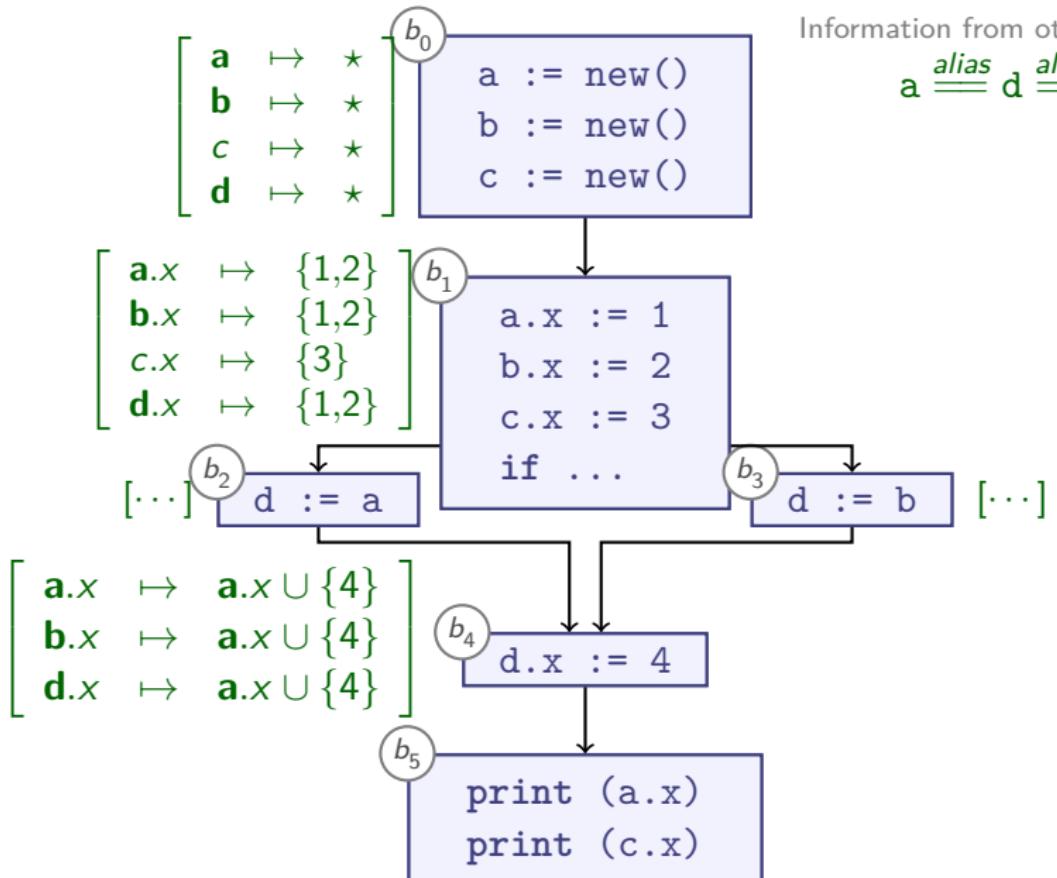
Dataflow with Alias Information



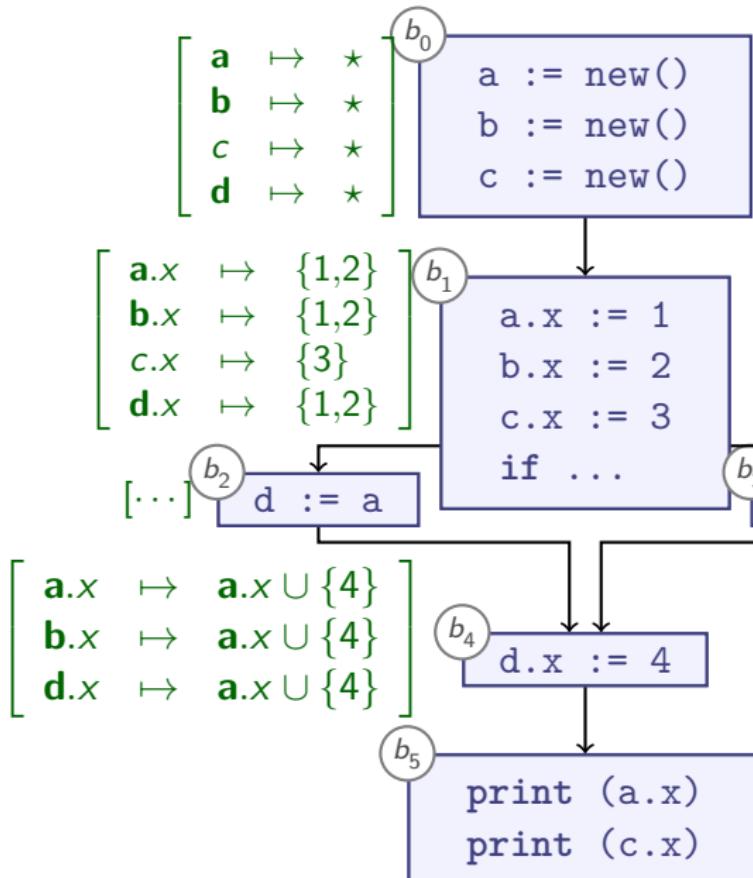
Dataflow with Alias Information



Dataflow with Alias Information



Dataflow with Alias Information



Information from other analysis:

$$a \xrightarrow{\text{alias}} d \xrightarrow{\text{alias}} b$$

Here, alias info is flow **insensitive**, so we must conservatively apply it to all basic blocks.

Alias info eliminates soundness problem.
Note: $d \xrightarrow{\text{alias}} a$ implies $d.x \xrightarrow{\text{alias}} a.x$.

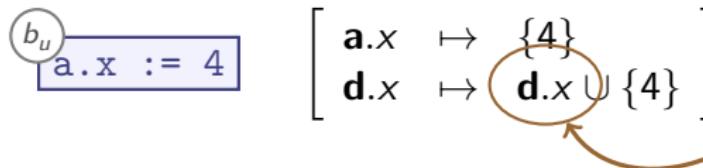
Dataflow + Aliases

- ▶ Aliasing affects shared fields:

$$a \xrightarrow{\text{alias}} d \implies a.x \xrightarrow{\text{alias}} d.x \text{ for all } x$$

- ▶ Use aliasing knowledge in one of these ways:

- 1 Multiply *updates* for each alias:



Using MAY alias info means that we might or might not update the aliased object.

- 2 Multiply *reads* for each alias:



- 3 Replace aliased paths by single representative (e.g., a represents both d and a):



Collaboration in Program Analysis



Analyses often form pipeline structures

Compute Aliases during Dataflow?

- ▶ Previously: Dataflow analysis as *analysis client* of Alias analysis:
- ▶ Can use Dataflow Analysis to compute pointer analyses
- ▶ Caveat:
 $y.\text{field} := z$
 - ▶ Transfer function updates: $y.\text{field} \mapsto z$
 - ▶ Must extract both y, z from \mathbf{in}_b to compute update
 - ▶ y, z may have aliases
 - ▶ *Non-distributive in general*

Summary

- ▶ **Analysis client:** user of analysis, often another analysis
 - ▶ E.g., *Type analysis* is client of *name analysis*
- ▶ **Alias analysis** helps make dataflow analysis more precise
 - ▶ Fields inherit aliasing:

$$a \xrightarrow{\text{alias}} b \implies a.x \xrightarrow{\text{alias}} b.x \text{ for all } x$$

- ▶ So if $a.x \xrightarrow{\text{alias}} b.y$, then:
 - ▶ $a.x.z \xrightarrow{\text{alias}} b.y.z$
 - ▶ $a.x.z.z \xrightarrow{\text{alias}} b.y.z.z$
 - ▶ $a.x.z.z.z \xrightarrow{\text{alias}} b.y.z.z.z$ etc.
- ▶ Dataflow analysis can compute pointer analyses
 - ▶ Requires non-distributive framework

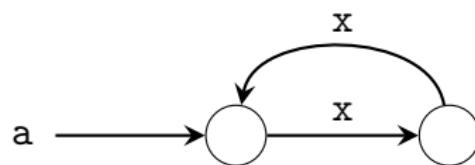
Concrete Heap Graphs (1/2)

Describe heap as a graph:

$$G_{\text{CHG}} = \langle \text{MemLoc}, \rightarrow, \xrightarrow{\blacksquare} \rangle$$

- ▶ G_{CHG} describes *actual* heap contents
- ▶ MemLoc are addressable memory locations
 - ▶ *Named* variables (a)
 - ▶ *Unnamed* variables (\bigcirc)
- ▶ Heap size typically ‘unbounded for all practical purposes’

```
a := new Obj();  
a.x := new Obj();  
a.x.x := a;
```



Concrete Heap Graphs (2/2)

- Direct points-to references:

$$(\rightarrow) : (Var \cup DynLoc) \rightarrow DynLoc$$

- Language difference:

- **Java/Teal**: Var is set of global / local variables and parameters

- Disjoint from $DynLoc$

- $HeapLoc = Var \cup DynLoc$

- **C/C++**: $Var = DynLoc = HeapLoc$

- Address-of operator (`&`) allows translating variable into $MemLoc$

- Points-to references via fields:

$$(\rightarrow^{\blacksquare}) : MemLoc \times Field \rightarrow MemLoc$$

- Field labels $Field$:

- E.g., `x` in '`a.x`' (Java) / '`a->x`' (C/C++)

- Array indices for '`a[10]`' (i.e., $\mathbb{N} \subseteq Field$)

Example

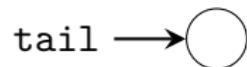
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fun makeList(len) {  
    var tail := new N();  
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    var body := tail;  
    while len > 0 {  
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        len := len - 1;  
    }  
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    return list;  
}
```

Example

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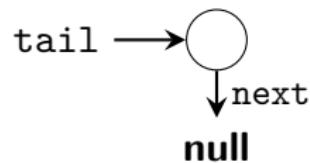
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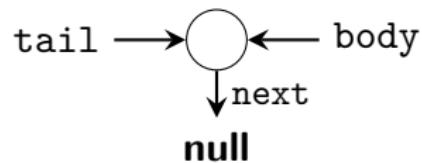
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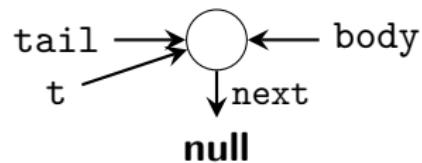
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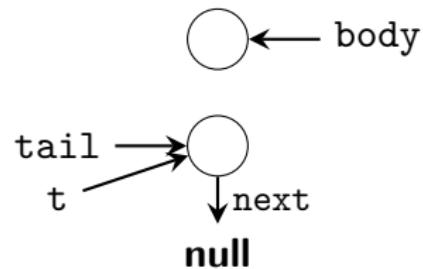
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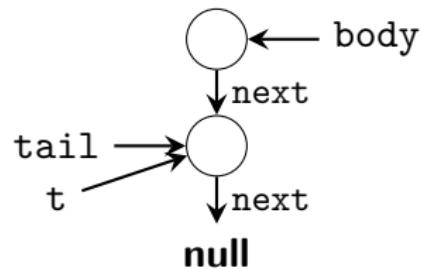
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Example

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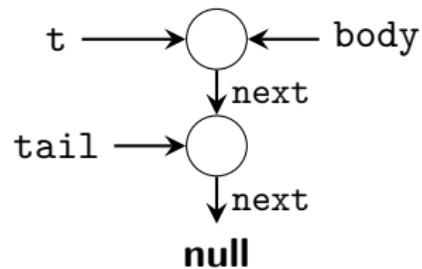
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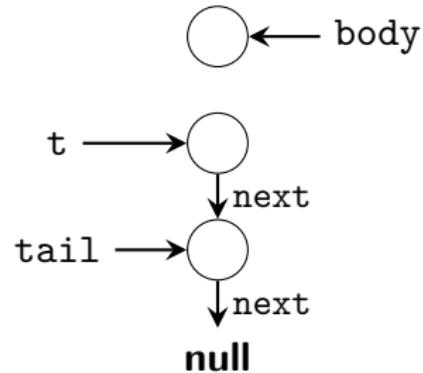
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Example

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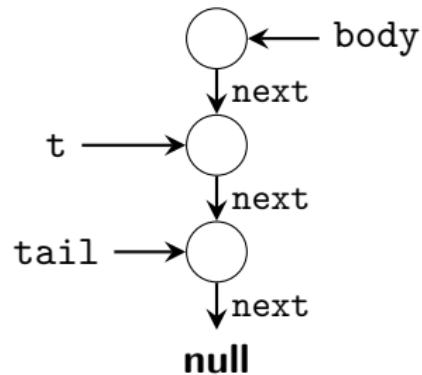
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Example

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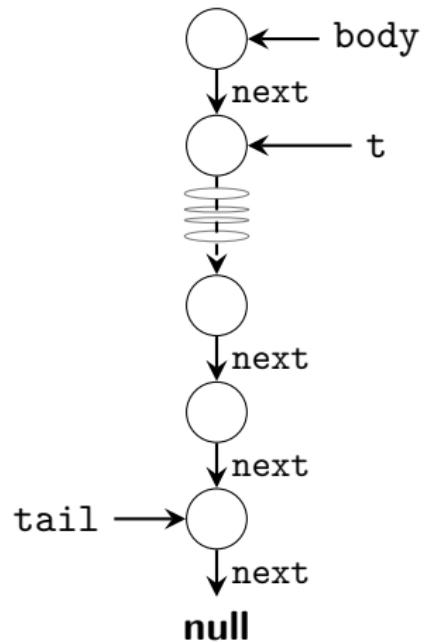
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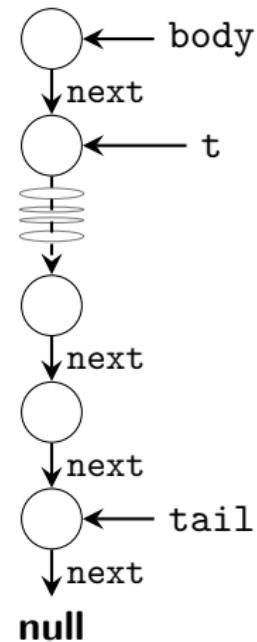
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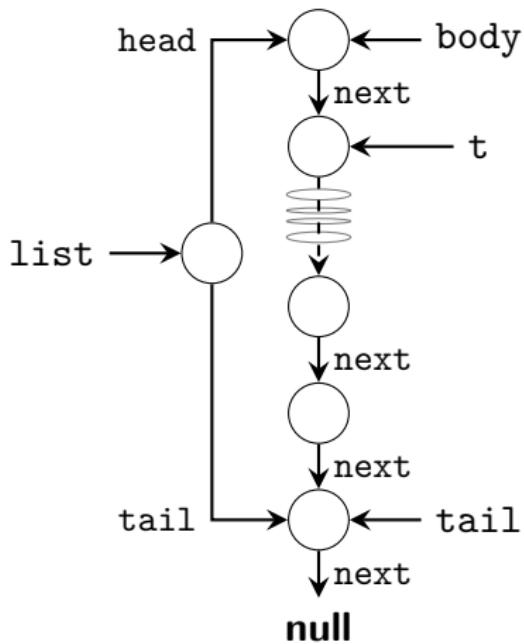


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len > 1:

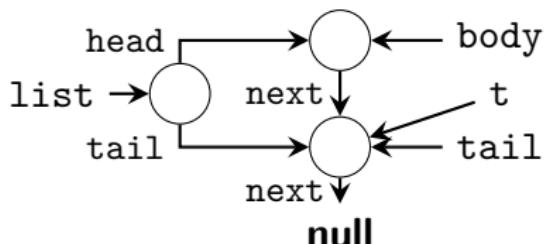


Example

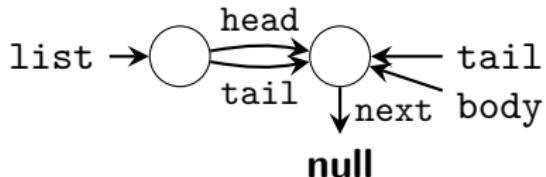
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}
```

len = 1:



len = 0:

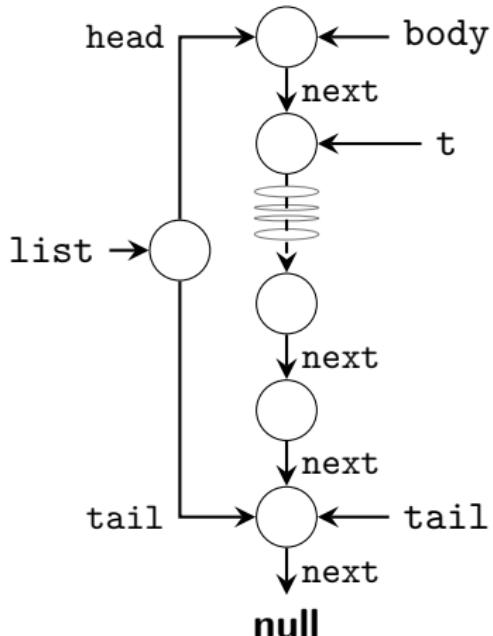


Managing Heap Graphs

- ▶ Size of Concrete Heap Graphs is unbounded
- ▶ Different parameters \implies different Concrete Heap Graphs
- ▶ **Store-less heap models:**
 - ▶ Hide heap locations
 - ▶ Model heap via *access paths*

list.head.next.next

Store-less Model



- ▶ Access path-based equivalences:
 - ▶ **Must:** $\text{list}.\text{tail} \stackrel{\text{alias}}{=} \text{tail}$
 - ▶ **Must:** $\text{list}.\text{head} \stackrel{\text{alias}}{=} \text{body}$
 - ▶ **Must:** $\text{body}.\text{next} \stackrel{\text{alias}}{=} t$
 - ▶ **May:** $\text{body}.\text{next}^* \stackrel{\text{alias}}{=} \text{tail}$
- ▶ Use *regular expressions* to denote repetition
- ▶ $\text{body}.\text{next}^*$ means:

body	$\stackrel{\text{alias}}{=}$	tail
body.next	$\stackrel{\text{alias}}{=}$	tail
body.next.next	$\stackrel{\text{alias}}{=}$	tail
...		
- ▶ For **May** or **Must** information

Summary

- ▶ **Concrete Heap Graph** (CHG) describes actual heap layout during execution
- ▶ CHG is unbounded, must summarise to analyse
- ▶ **Store-less Models:**
 - ▶ Use **access paths** to describe memory locations
 - ▶ Common in alias analysis

Managing Heap Graphs

- ▶ Size of Concrete Heap Graphs is unbounded
- ▶ Different parameters \implies different Concrete Heap Graphs
- ▶ **Store-less heap models:**
 - ▶ Hide heap locations
 - ▶ Model heap via *access paths*

list.head.next.next

- ▶ **Store-based heap models:**
 - ▶ Keep heap locations explicit
 - ▶ Introduce *Summary nodes* that can describe multiple CHG nodes

Store-based Model

- Concrete Heap Graph (CHG): graph of the program's reality

$$G_{\text{CHG}} = \langle \text{MemLoc}, \rightarrow, \xrightarrow{\blacksquare} \rangle$$

- Abstract Heap Graph (AHG): approximation of the program's reality

$$G_{\text{AHG}} = \langle \mathcal{P}(\text{MemLoc}), \rightarrow, \xrightarrow{\blacksquare} \rangle$$

$$(\xrightarrow{\textcolor{blue}{\square}}) : \mathcal{P}(\text{MemLoc}) \rightarrow \mathcal{P}(\text{MemLoc})$$

$$(\xrightarrow{\blacksquare}) : \mathcal{P}(\text{MemLoc}) \times \mathcal{P}(\text{Field}) \rightarrow \mathcal{P}(\text{MemLoc})$$

- Key idea: AHG is *finite* graph that summarises CHG

- Soundness via:

$$v \rightarrow \ell \quad \text{implies} \quad \{v\} \cup V' \xrightarrow{\rightarrow} \{\ell\} \cup L'$$

$$\ell_0 \xrightarrow{f} \ell_1 \quad \text{implies} \quad \{\ell_0\} \cup L'_0 \xrightarrow{\textcolor{blue}{\{f\} \cup F'}} \{\ell_1\} \cup L'_1$$

- 'Any CHG edge is represented by (at least) one AHG edge'

Summary Nodes and Edges

Notation:

- Abstract node $N \subseteq \text{MemLoc}$:

- $|N| = 1$: *precise*:
- $|N| > 1$: *summary*:

- Consider edge $V \rightarrow L$:

- $|V| = 1$: *precise*:

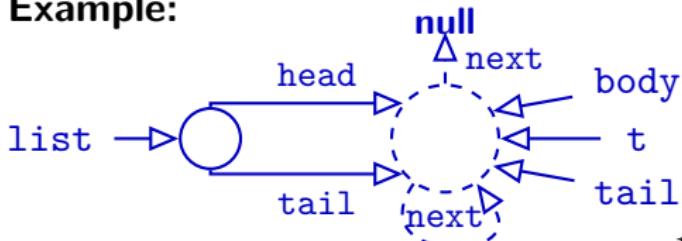
$V \longrightarrow L$

- $|V| > 1$: *summary*:

$V \dashrightarrow L$

- Analogous for $(\rightarrow f)$

Example:



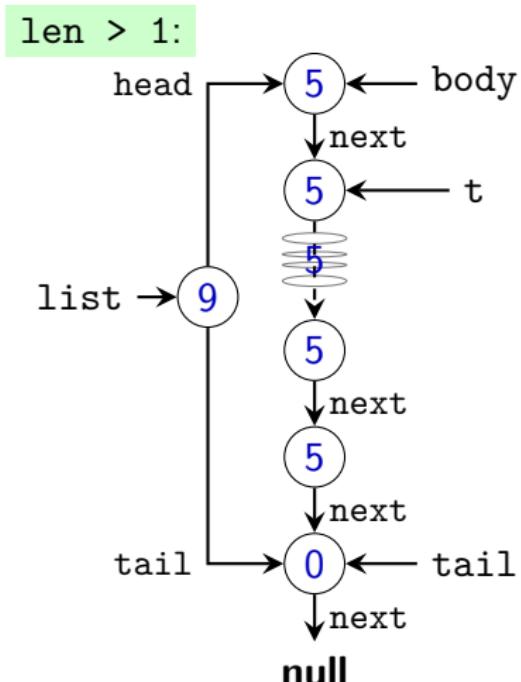
Summary

- ▶ **Store-based Models:**
 - ▶ Use **Abstract Heap Graph** to summarise *Concrete Heap Graph*
 - ▶ Common for finding memory bugs
 - ▶ Represents NFA
 - ▶ Equivalent to regular expressions

Summaries from Allocation Sites

Teal-2

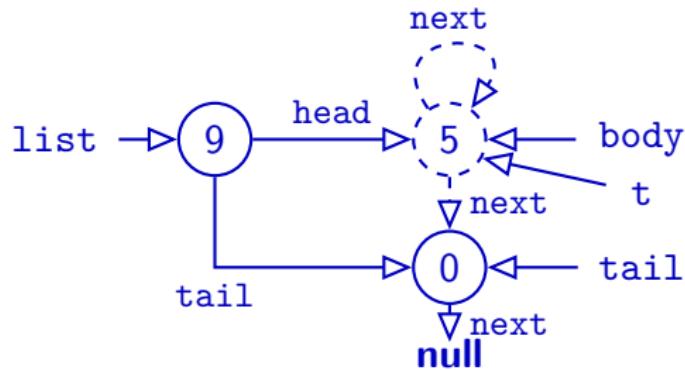
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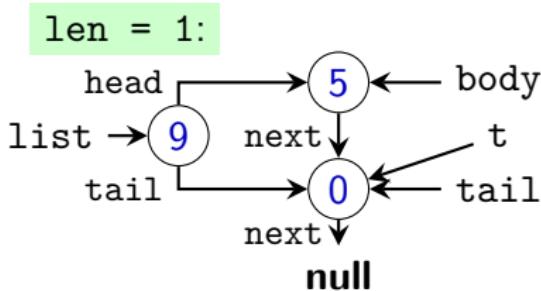
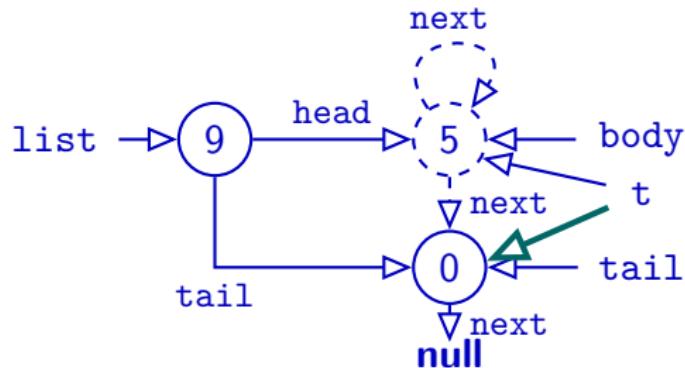


- ▶ Summarise *MemLoc* allocated at same program location

Summaries from Allocation Sites

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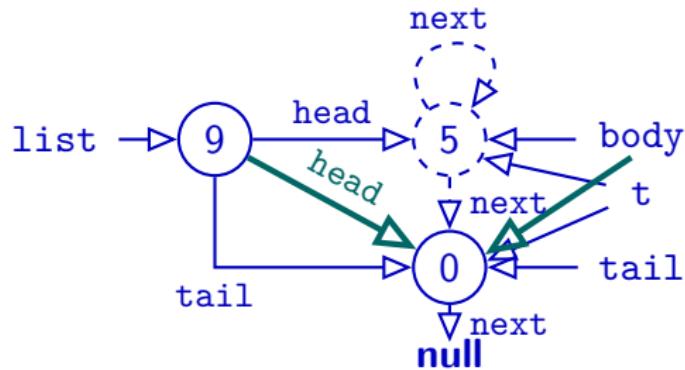


- ▶ Summarise *MemLoc* allocated at same program location
- ▶ Nodes can have multiple outgoing arrows

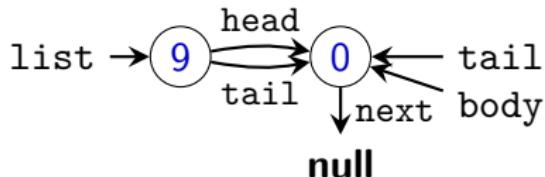
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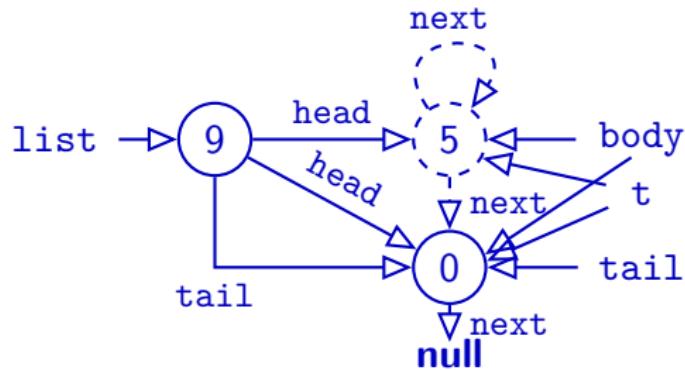


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- ▶ Summarise *MemLoc* allocated at same program location
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Summaries via k -Limiting

- ▶ k -Limiting: bound size
- ▶ Examples: Limiting...

- ▶ Access path length

Example ($k=3$):

list.head.next	⇒	list.head.next
list.head.next.next	⇒	list.head.next*
list.head.next.next.next	⇒	list.head.next*
list.head.next.next.val	⇒	list.head.(val next)*

- ▶ # of (\rightarrow) hops after named variable
- ▶ # of nodes transitively reachable via (\rightarrow) after named variable
- ▶ # of nodes in a loop / function body

...

Other Summary Techniques

- ▶ General idea: Map $\mathcal{P}(MemLoc)$ to finite (manageable!) set
 - ▶ Can combine different techniques for increased precision
 - ▶ Other techniques: distinguish heap nodes by:
 - ▶ How many edges point to the node?
 - ▶ Is the node in a cycle?
 - ▶ What is the type of the node? (`ArrayList`,
 `StringTokenizer`, `File`, ...)
- ...

Design Considerations

- ▶ First goal remains: make output finite
- ▶ Useful for analysis clients
- ▶ Efficient to compute / represent
- ▶ When considering flow-sensitive models:
 - ▶ Different program locations will have different AHGs
 - ▶ Exploit sharing across program locations

Summary of Heap Summaries

- ▶ *Store-less Models*:
 - ▶ Common in alias analysis
- ▶ **Store-based Models**:
 - ▶ Use **Abstract Heap Graph** to summarise *Concrete Heap Graph*
 - ▶ Common for finding memory bugs
 - ▶ NFA representation \mapsto regular expressions for Access Paths
- ▶ Summarisation techniques:
 - ▶ **Allocation-Site Based**: summarise nodes allocated at same point in program
 - ▶ **k -Limiting**: Set bound on some property P : no more than k P s allowed
 - ▶ Many combinations / extensions conceivable